

Press release

2005 September

Release of a visual programming software for pre-processing in hardware

VisualApplets is a graphically oriented tool, which simplifies the programming of image pre-processing on FPGAs.

Visual Applet is a tool for hardware programming on FPGA, based on graphic data flows. These data flows are arranged by the combination of operators and filter modules of an image processing library and are compiled to a loadable hardware applet.

The library contains arithmetical and morphological operators for pixel manipulation, logical operators for classification tasks and also complex modules e.g. colour processing and compression.

There is no configuration necessary for synchronization, timing or avoiding problems with side-effects. Both a synthesis and a high level simulation are integrated and offer full control over the final visual result of the application at any time.

The complete process of the hardware design creation lasts 10-15 minutes on an average. The hardware applet can immediately be used with the configuration software microDisplay or integrated in application by the SDK. Although knowledge of hardware programming is advantageous, the software solution Visual Applet is addressed to software programmers in Machine Vision as a matter of priority. In first approximation Visual Applet is hardware-independent. At present microEnable III product line of **SILICON SOFTWARE** is the supported hardware platform. Visual Applets runs under Windows 2000 and XP.

Figure:
Data flow model type in VisualApplets



VisualApplet_Surface.tif, CMYH, 300dpi, 1069*639 Pixels

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Silicon Software GmbH, located in Mannheim/Germany, is a manufacturer of intelligent pre-processing solutions based on reprogrammable FPGA technology for machine vision applications. The hard- and software products are designed for flexibility, easy handling and performance featuring user programmable FPGA technology.